



# ARC'2007 International Workshop on Applied Reconfigurable Computing

<http://www.arc-workshop.org/arc2007>

## Call for Papers

### Mangaratiba, Rio de Janeiro, Brazil, March 27-29, 2007

#### MEETING VENUE

The workshop will be held at the **Portobello Hotel** in Mangaratiba, Rio de Janeiro, Brazil, on March 27-29, 2007. Mangaratiba is located approx. 90 Km (56 miles) from downtown Rio de Janeiro.

The event is organized by the Instituto de Ciências Matemáticas e de Computação (ICMC), Universidade de São Paulo, Brazil.

#### ORGANIZING COMMITTEE

##### General Chairs

Eduardo Marques, University of São Paulo, Brazil  
Koen Bertels, Delft University of Technology, The Netherlands

##### Program Chair

Pedro C. Diniz, IST/INESC-ID, Portugal

##### Steering Committee

George Constantinides, Imperial College, UK  
João M. P. Cardoso, IST/INESC-ID, Portugal  
Koen Bertels, Delft Univ. of Technology, The Netherlands  
Mladen Berekovic, IMEC vzw, Leuven, Belgium  
Pedro C. Diniz, IST/INESC-ID, Portugal  
Stamatis Vassiliadis, Delft Univ. of Technology, The Netherlands  
Walid Najjar, University of California Riverside, USA

##### Proceedings Chair

Marcio M. Fernandes, Unimep, Brazil

##### Special Journal Edition Chairs

George Constantinides, Imperial College, UK  
João M. P. Cardoso, IST/INESC-ID, Portugal

##### Publicity Chair

Fernanda G. L. Kastensmid, UFRGS, Brazil

##### Sponsorship Chair

Denis F. Wolf, University of São Paulo, Brazil

##### Finance Chair

Jorge L. Silva, University of São Paulo, Brazil

##### Local Arrangements Chairs

Marcos Santana, University of São Paulo, Brazil  
Regina C. Santana, University of São Paulo, Brazil  
Ricardo Menotti, UTFPR, Brazil  
Vanderlei Bonato, University of São Paulo, Brazil

##### Web Chair

Carlos Almeida Jr., University of São Paulo, Brazil

##### Secretariat

Marília Marino, University of São Paulo, Brazil

#### PROGRAM COMMITTEE

Andreas Koch, TU Darmstadt, Germany  
Andy Pimentel, University of Amsterdam, The Netherlands  
António Ferrari, University of Aveiro, Portugal  
Bernard Pottier, University of West Brittany (UBO), France  
Carl Ebeling, University of Washington, USA  
Eduardo Marques, University of São Paulo, Brazil  
George Constantinides, Imperial College, UK  
Hideharu Amano, Keio University, Japan  
Horácio Neto, INESC-ID/IST, Portugal  
Jeff Arnold, Stretch Inc., USA  
Joachim Pistorius, Altera Corp., USA  
João M. P. Cardoso, IST/INESC-ID, Portugal  
Joon-seok Park, Inha University, Seoul, South Korea  
José Nelson Amaral, University of Alberta, Canada  
José Sousa, IST/INESC-ID, Portugal  
Juan Carlos de Martin, Politecnico de Torino, Italy  
Jürgen Becker, University of Karlsruhe (TH), Germany  
Koen Bertels, Delft Univ. of Technology, The Netherlands  
Laura Pozzi, University of Lugano (USI), Switzerland  
Marco Platzner, University of Paderborn, Germany  
Markus Weinhardt, PACT XPP Technologies AG, Germany  
Maria-Cristina Marinescu, IBM T. J. W. Research Center, USA  
Mihai Budiu, Microsoft Research, USA  
Mladen Berekovic, IMEC vzw, Leuven, Belgium  
Nader Bagherzadeh, University of California, Irvine, USA  
Oliver Diessel, University of New South Wales, Australia  
Paul Chow, University of Toronto, Canada  
Pedro C. Diniz, IST/INESC-ID, Portugal  
Pedro Trancoso, University of Cyprus, Cyprus  
Peter Cheung, Imperial College, UK  
Phil James-Roxby, Xilinx Corp., USA  
Philip Leong, The Chinese University of Hong Kong, China  
Ranga Vemuri, University of Cincinnati, USA  
Reiner Hartenstein, University of Kaiserslautern, Germany  
Roger Woods, The Queen's University of Belfast, UK  
Roman Hermida, Universidad Complutense, Madrid, Spain  
Russell Tessier, University of Massachusetts, USA  
Ryan Kastner, University of California, Santa Barbara, USA  
Seda Ö. Memik, Northwestern University, USA  
Stamatis Vassiliadis, Delft Univ. of Technology, The Netherlands  
Stephan Wong, Delft University of Technology, The Netherlands  
Tarek El-Ghazawi, The George Washington University, USA  
Tim Callahan, Carnegie Mellon University, USA  
Tsutomu Sasao, Kyushu Institute of Technology, Japan  
Walid Najjar, University of California Riverside, USA  
Wayne Luk, Imperial College, UK

#### APPLIED RECONFIGURABLE COMPUTING

Reconfigurable computing technologies offer the promise of substantial performance gains over traditional architectures via the customizing, even at runtime, the topology of the underlying architecture to match the specific needs of a given application. Contemporary configurable architectures allow for the definition of architectures with functional and storage units that match in function, bit-width and control structures the specific needs of a given computation. The flexibility enabled by reconfiguration is also seen as a basic technique for overcoming transient failures in emerging device structures.

ARC aims at bringing together researchers and practitioners of reconfigurable computing with an emphasis on practical applications of this promising technology. This year's workshop will have a series of international invited speakers that will express their view on the future of reconfigurable technology.

#### WORKSHOP THEMES

##### Topics of Interest

Submissions are solicited on a wide variety of topics related to applied reconfigurable computing, including but not limited to:

- Methods and Tools (High-Level Compilers, Simulation, Estimation, Design space exploration, Languages to program reconfigurable systems, etc.)
- Architectures (Fine-grained, coarse-grained, and mixed-grained, Multi-processor-based reconfigurable platforms, Microprocessors with tightly-coupled reconfigurable hardware, etc.)
- Applications (High-Performance Systems, use of reconfigurable computing in embedded systems, robotics, digital signal processing, etc.)
- Teaching reconfigurable computing
- Surveys and Future Trends
- Benchmarks (papers presenting benchmarks publicly available to be used by the reconfigurable computing community are especially welcome)

#### SUBMISSION INFORMATION

Authors are invited to submit original contributions in English including, but not limited to, the areas of interest mentioned above. Papers should be submitted electronically in PDF format following the LNCS formatting guidelines (see <http://www.springer.de/comp/lncs/authors.html>).

Submissions must identify the format of the contribution as either, **long papers**, 12 pages maximum and which should include mainly accomplished results, or as **short papers** (6 pages maximum to be composed of work in progress or report recent developments). The paper submission website is located at <http://conferenceserver.rnl.ist.utl.pt/conftool> which can also be accessed via the ARC 2007 website indicated above. Each paper will be reviewed by at least three program committee members. In order to maintain a blind review information about authors should not be included in the submission.

#### IMPORTANT DATES

**Submission deadline:** November 20, 2006

**Author Notification:** December 22, 2006

**Camera-Ready deadline:** January 8, 2007

**Registration:** December 29, 2006

**Workshop:** 27-29, March, 2007

For more information:  
[arc2007@icmc.usp.br](mailto:arc2007@icmc.usp.br)

#### Proceedings Publication

As with the 2006 ARC edition, it is planned to publish the proceedings of this year's workshop as a **Springer Verlag LNCS series volume**. The best papers will be invited to submission to a special issue of the **International Journal of Electronics** (Taylor & Francis Group) dedicated to ARC 2007. Please use the LaTeX instructions file LLNCS.zipfile located at <http://www.springer.com/sgw/cda/frontpage/0,11855,5-164-2-72376-0,00.html>.

#### SATELLITE EVENT

**RMRC - Regional Meeting on Reconfigurable Computing** ([www.icmc.usp.br/~lcr/rmrc2007](http://www.icmc.usp.br/~lcr/rmrc2007))  
Mangaratiba, Rio de Janeiro, Brazil, March 26, 2007